MTP3055VL

Preferred Device

Power MOSFET 12 Amps, 60 Volts, Logic Level

N-Channel TO-220

This Power MOSFET is designed to withstand high energy in the avalanche and commutation modes. Designed for low voltage, high speed switching applications in power supplies, converters and power motor controls, these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional safety margin against unexpected voltage transients.

- Avalanche Energy Specified
- I_{DSS} and V_{DS(on)} Specified at Elevated Temperature

MAXIMUM RATINGS (T_C = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	60	Vdc
Drain-Gate Voltage ($R_{GS} = 1.0 \text{ M}\Omega$)	V_{DGR}	60	Vdc
Gate-Source Voltage - Continuous - Single Pulse (t _p ≤ 50 μs)	V _{GS} V _{GSM}	±15 ± 20	Vdc Vpk
Drain Current – Continuous @ 25°C – Continuous @ 100°C – Single Pulse (t _p ≤ 10 μs)	I _D I _D	12 8.0 42	Adc Apk
Total Power Dissipation @ 25°C Derate above 25°C	P _D	48 0.32	Watts W/°C
Operating and Storage Temperature Range	T _J , T _{stg}	-55 to 175	°C
Single Pulse Drain-to-Source Avalanche Energy – Starting T_J = 25°C (V_{DD} = 25 Vdc, V_{GS} = 5.0 Vdc, I_L = 12 Apk, L = 1.0 mH, R_G =25 Ω)	E _{AS}	72	mJ
Thermal Resistance - Junction to Case - Junction to Ambient	R _{eJC} R _{eJA}	3.13 62.5	°C/W
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	TL	260	°C

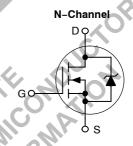


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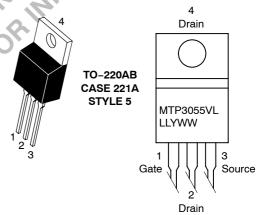
http://onsemi.com

12 AMPERES 60 VOLTS

 $R_{DS(on)} = 180 \text{ m}\Omega$



MARKING DIAGRAM & PIN ASSIGNMENT



MTP3055VL = Device Code

LL = Location Code

Y = Year

WW = Work Week

ORDERING INFORMATION

Device	Package	Shipping
MTP3055VL	TO-220AB	50 Units/Rail

Preferred devices are recommended choices for future use and best overall value.

MTP3055VL

ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise noted)

Characteris	racteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS		5,501		. , , ,	ax	J.m.
Drain-Source Breakdown Voltage (V _{GS} = 0 Vdc, I _D = 250 μAdc) Temperature Coefficient (Positiv		V _{(BR)DSS}	60 –	- 62	- -	Vdc mV/°C
Zero Gate Voltage Drain Current $ (V_{DS} = 60 \text{ Vdc}, V_{GS} = 0 \text{ Vdc}) \\ (V_{DS} = 60 \text{ Vdc}, V_{GS} = 0 \text{ Vdc}, T_J = 150^{\circ}\text{C}) $		I _{DSS}	<u>-</u>	- -	10 100	μAdc
Gate-Body Leakage Current (V _{GS} = ±15 Vdc, V _{DS} = 0)		I _{GSS}	-	-	100	nAdc
ON CHARACTERISTICS (Note 1)						
Gate Threshold Voltage $(V_{DS} = V_{GS}, I_D = 250 \mu\text{Adc})$ Temperature Coefficient (Negative)		V _{GS(th)}	1.0	1.6 3.0	2.0 –	Vdc mV/°C
Static Drain-Source On-Resistan	ce (V _{GS} = 5.0 Vdc, I _D = 6.0 Adc)	R _{DS(on)}	_	0.12	0.18	Ohm
Drain-Source On-Voltage (V_{GS} = (I_D = 12 Adc) (I_D = 6.0 Adc, T_J = 150°C)	5.0 Vdc)	V _{DS(on)}	- -	1.6 -	2.6 2.5	Vdc
Forward Transconductance (V _{DS}	= 8.0 Vdc, I _D = 6.0 Adc)	g _{FS}	5.0	8.8	-	mhos
DYNAMIC CHARACTERISTICS			4, 3	0	7	
Input Capacitance		C _{iss}	-7	410	570	pF
Output Capacitance	$(V_{DS} = 25 \text{ Vdc}, V_{GS} = 0 \text{ Vdc}, f = 1.0 \text{ MHz})$	C _{oss}	.Ci	114	160	
Reverse Transfer Capacitance		C _{rss}	11-7	21	40	
SWITCHING CHARACTERISTICS	(Note 2)	0 0	Oly,			
Turn-On Delay Time		t _{d(on)}	NO.	9.0	20	ns
Rise Time	$(V_{DD} = 30 \text{ Vdc}, I_D = 12 \text{ Adc},$	t _r	_	85	190	
Turn-Off Delay Time	$V_{GS} = 5.0 \text{ Vdc},$ $R_G = 9.1 \Omega)$	t _{d(off)}	-	14	30	
Fall Time	1110 011	tf	-	43	90	
Gate Charge		Q _T	-	8.1	10	nC
(See Figure 8)	(V _{DS} = 48 Vdc, I _D = 12 Adc,	Q ₁	-	1.8	-	
	V _{GS} = 5.0 Vdc)	Q ₂	-	4.2	-	-
		Q_3	_	3.8	-	-
SOURCE-DRAIN DIODE CHARAC	CTERISTICS		I.	I.		
Forward On-Voltage (Note 1)	$(I_S = 12 \text{ Adc}, V_{GS} = 0 \text{ Vdc})$ $(I_S = 12 \text{ Adc}, V_{GS} = 0 \text{ Vdc}, T_J = 150^{\circ}\text{C})$	V _{SD}	_ _	0.97 0.86	1.3 -	Vdc
Reverse Recovery Time		t _{rr}	-	55.7	-	ns
(See Figure 14)		t _a	-	37	-	
X.	$(I_S = 12 \text{ Adc}, V_{GS} = 0 \text{ Vdc}, \\ dI_S/dt = 100 \text{ A}/\mu\text{s})$	t _b	-	18.7	-	
Reverse Recovery Stored Charge	, , ,	Q _{RR}	-	0.116	1	μC
INTERNAL PACKAGE INDUCTAN	CE					
Internal Drain Inductance (Measured from contact screw of (Measured from the drain lead of	on tab to center of die) 0.25" from package to center of die)	L _D	_	3.5 4.5	-	nH
Internal Source Inductance (Measured from the source lead	l 0.25" from package to source bond pad)	L _S	-	7.5	ı	nH

Pulse Test: Pulse Width ≤300 μs, Duty Cycle ≤ 2%.
 Switching characteristics are independent of operating junction temperature.